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DECLARATION IN ORIGINAL APPLICATION

Attorney Docket No.
S01.12-0734/STL 9752

SPECIFICATION AND INVENTORSHIP IDENTIFICATION

As a below named inventor, I declare that:

My residence, post office address and citizenship are as stated below next to my name.

I believe I am the original, first and sole inventor of the subject matter which is claimed, and for which a patent is sought, on the invention entitled TECHNIQUE FOR MEASURING SMALL DISTANCES BETWEEN, AND FOR MEASURING THE FLATNESS OF, ELECTRICALLY CONDUCTIVE SURFACES the specification of which,

x is entitled TECHNIQUE FOR MEASURING SMALL DISTANCES BETWEEN, AND FOR MEASURING THE FLATNESS OF, ELECTRICALLY CONDUCTIVE SURFACES, having an attorney docket number S01.12-0734/STL 9752.

_____ was filed on _____ as Appln. No. _____
_____ and was amended on _____
_____ was described and claimed in PCT International Application No. _____ filed on _____ and as amended under PCT Article 19 on _____.

ACKNOWLEDGEMENT OF REVIEW OF PAPERS AND DUTY OF CANDOR

I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above. I acknowledge the duty to disclose information which is known to me to be material to the patentability of this application in accordance with 37 C.F.R. § 1.56.

PRIORITY CLAIM (35 U.S.C. § 119)

Prior Foreign Application(s)

I claim foreign priority benefits under 35 U.S.C. § 119(a-d) of any foreign application(s) for patent or inventor's certificate listed below, each of which is incorporated by reference in its entirety, and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

Number	Country	Day/Month/Year Filed	Priority Claimed
_____	_____	_____	Yes _____ No _____
_____	_____	_____	Yes _____ No _____

Prior Provisional Application(s)

I hereby claim the benefit under 35 U.S.C. §119(e) of any United States Provisional Application(s) listed below, each of which is incorporated by reference in its entirety:

Number	Day/Month/Year Filed
60/221,532	July 28, 2000
_____	_____

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PRIORITY CLAIM (35 U.S.C. § 120)

I claim the benefit under 35 U.S.C. § 120 of any United States application(s) listed below, each of which is incorporated by reference in its entirety. Insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of 35 U.S.C. § 112, I acknowledge the duty to disclose to the Patent Office all information known to me to be material to patentability as defined in 37 C.F.R. § 1.56 which became available between the filing date of the prior application and the national or PCT international filing date of this application:

Appln. No.	U.S. Appln. No. (if any under PCT)	Filing Date	Status
_____	_____	_____	_____
_____	_____	_____	_____

DECLARATION

I declare that all statements made herein that are of my own knowledge are true and that all statements that are made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under 18 U.S.C. § 1001 and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

DESIGNATION OF CORRESPONDENCE ADDRESS

Please address all correspondence and telephone calls to Alan G. Rego in care of:

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Inventor: Matthew M. Johnston Date: 7-26-2001
(Signature)

Inventor: Matthew M. Johnston
(Printed Name)

Residence: Edina, MN Citizenship: U.S.A.

P.O. Address: 7350 Gallagher Drive #137A, Edina, MN 55435

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s):	Matthew M. Johnston	Examiner:	
Serial No.:		Group Art Unit	
Filed:	HEREWITH		
Title:	TECHNIQUE FOR MEASURING SMALL DISTANCES BETWEEN, AND FOR MEASURING THE FLATNESS OF, ELECTRICALLY CONDUCTIVE SURFACES		
Docket:	S01.12-0734/STL 9752		

**POWER OF ATTORNEY BY ASSIGNEE OF ENTIRE INTEREST
(REVOCATION OF PRIOR POWERS)**

As assignee of record of the entire interest of the above identified application,
REVOCATION OF PRIOR POWERS OF ATTORNEY

all powers of attorney previously given are hereby revoked and

NEW POWER OF ATTORNEY

the following practitioners/patent agent are hereby appointed to prosecute and transact all business in the Patent and Trademark Office connected therewith.

Shawn B. Dempster, Registration No. 34,321	Edward P. Heller, III, Registration No. 29,075
Jonathan E. Olson, Registration No. 41,231	Raghunath S. Minisandram, Registration No. 38,683
Derek J. Berger, Registration No. 45,401	Carol I. Bordas, Registration No. 37,284
Kirk A. Cesari, Registration No. 47,479	Paul T. Dietz, Registration No. 38,858
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And members of the firm of Westman, Champlin & Kelly, P.A.:

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STATEMENT UNDER 37 CFR 3.73(b)

Seagate Technology LLC states that it is the Assignee of Entire Interest in the patent application/patent identified above by virtue of an Assignment from the inventor(s) of the patent application/patent identified above. A copy of the Assignment is attached and/or was recorded in the Patent and Trademark Office at Reel , Frame . The undersigned (whose title is supplied below) is empowered to sign this statement on behalf of the Assignee.

Respectfully submitted,

SEAGATE TECHNOLOGY LLC
(Assignee of Entire Interest)

July 27, 2001
Date



Paul T. Dietz, Reg. No. 38,858

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